Multi-Mode Excitation by Interleaved EBG Structure for Suppression of Power/Ground Noise in Multi-Layer PCBs

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Abstract – Signal line transition with layer transition via is inevitable in multi-layer PCB. The return current can generate voltage noise between the cavities due to the discontinuity of the return current path. Other layer transition vias passing through the cavity can pick up the voltage noise and result in problems of signal integrity. In this paper, an electromagnetic bandgap (EBG) structure is proposed for suppression of the broadband cavity noise. The impedance discontinuity between layers of interleaved EBG cell enhances the efficiency of noise suppression, and the slots embedded in the EBG cell excite multi-mode resonances for extending the bandwidth of noise suppression. The dispersion diagram is utilized to preliminarily analyze the characteristic of the proposed EBG cell, and a 5×5 cells EBG board is further analyzed for characterizing the efficiency of noise suppression. Both simulation and measurement results prove the proposed structure can effectively suppress the cavity noise under −35 dB over the frequency range from 0.56 GHz to the highest measurement frequency, 20 GHz.

Index Terms – Electromagnetic bandgap, microwave filter, power/ground noise, signal integrity.

I. INTRODUCTION

With increasing data rate of high-speed digital signal, the nonideal return current path can result in severe power integrity as well as signal integrity problems. The return current with low impedance path is required for the layer transition via passing through multiple layers, as the return current flows through the impedance of the cavity made up of two planes can create a voltage between the planes [1]. The created voltage can propagate in the cavity and should be minimized and suppressed because other layer transition vias passing through the cavity can pick up this voltage noise. To provide a low impedance path for the return current of the layer transition via is an effective approach to suppress the cavity noise and can be achieved by placing decoupling capacitors between power and ground planes [2]. However, the effective inductance (ESL) of the capacitor component limits the bandwidth of the low impedance path of return current. For suppressing the propagation of cavity noise over the effective frequencies limited by the decoupling capacitor, a lot of electromagnetic bandgap(EBG) structures have been proposed to isolate the cavity noise for the GHz application [3–5]. Due to the demand for high density design, an EBG structure with double stacked patches has been proposed for achieving compact size and broadband isolation [6]. That structure is designed with the mushroom-like EBG structure so that it can be practically integrated into existing PCB designs without the issue of IR drop.

In order to further reduce the size and extend the effective bandwidth of the EBG structure, an interleaved EBG structure with double-stacked patches has been proposed [7]. The miniaturization is achieved by shunt capacitance of the interleaved patches, and the bandwidth is optimized by the location of vias connected to the patches. However, not only the layer transition via that carries high frequency components of high speed digital signals but also the layer transition via that connects to input node or switching node of switched-mode power supplies (SMPS) can inject noise into the cavity [8]. The switching power noise is typically lower than 1 GHz and the Nyquist frequency of the high-speed digital signal can be 28 GHz for 112 Gbps application with PAM4 signaling. These cavity noises can simultaneously couple to any signal passing through the cavity [9]. The signal that picks up cavity noise can be a severe problem of signal integrity and then further results in radiated emission, so that the EBG structure with wider stopband bandwidth and high isolation for suppression of cavity noise is needed.

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An EBG structure based on the mushroom-like EBG structure with interleaved and double stacked patches in a four-layer stack-up is proposed in this paper. The impedance discontinuity between layers of interleaved EBG cell enhances the isolation for suppression of the noise propagation. The slots embedded in the EBG cell are induced to further extend the stopband bandwidth. The characteristic of the proposed EBG structure is measured by vector network analyzer and simulated by commercial field solvers. Both simulation and measurement present excellent results of suppression of noise propagation by improved stopband bandwidth and isolation. The design concept and procedure will be described in Section II. The simulation and measurement results of the proposed structure are shown in Section III. Finally, the conclusion is presented in Section IV.

II. DESIGN CONCEPT AND PROCEDURE

A. Suppression of propagation mode by discontinuity of impedance between plane pairs

The reference design shown in Figure 1 is based on the EBG cell with geometrical parameters \((a, b, d, h_1, h_2) = (12 \text{ mm}, 11.8 \text{ mm}, 0.8 \text{ mm}, 0.1 \text{ mm}, 0.1 \text{ mm})\) for the PCB made of FR-4 material with relative permittivity \(\varepsilon_r = 4.4\). The interleaved patches are double stacked in a four-layer stack-up. All the thicknesses of dielectric as well as the capacitance between adjacent planes are the same in the reference design. In order to extend the stopband bandwidth to lower frequency, the thickness of the dielectric, \(h_2\), between the second and the third layers is increased from 0.1 to 0.8 mm. Therefore, the via barrel is lengthened and the inductance of the via is increased accordingly. As the capacitances between the first and second layers and the third and fourth layers remain the same, the increased inductance shifts the resonant frequency to lower frequency for extending the stopband bandwidth. The thickened dielectric between the second and the third layers also decreases the capacitance and results in the impedance discontinuities, so that the isolation for suppression of the noise propagation is enhanced. Figure 2 shows dispersion diagrams of the EBG cells of the reference design and that with thickened dielectric, respectively. For the result of the reference design, the bandgap is over the frequency range from 0.87 to 2.82 GHz except a propagating mode at 2.06 GHz. The bandgap of the design with thickened dielectric is from 0.59 to 2.95 GHz and its propagation mode is moved from 2.06 to 1.14 GHz. A \(5 \times 5\) cells EBG board shown in Figure 3 is used to further analyze the effectiveness of isolation for the practicality. The cavity noise is excited at (18 mm, 3 mm) and the observation point is at (42 mm, 51 mm). Figure 4 shows that the design with thickened dielectric not only moves the propagation mode from 2.06 to 1.14 GHz but also enhances the isolation for suppression of the noise propagation. The cavity noise is suppressed under –35 dB from 0.53 to 5.77 GHz.

B. Suppression of propagation mode by patches with slots

In order to suppress propagation modes for frequencies higher than 5.77 GHz, a novel EBG cell shown in Figure 5 is proposed for extending stopband bandwidth by excited multiple modes. The patches for the design shown in Figure 1 with geometrical parameters \((a, b, d, h_1, h_2) = (12 \text{ mm}, 11.8 \text{ mm}, 0.8 \text{ mm}, 0.1 \text{ mm}, 0.1 \text{ mm})\) are replaced by the proposed EBG cells with geometrical parameters \((g, w) = (0.1 \text{ mm}, 0.2 \text{ mm})\). The slots need to be located at the edges of the patch as the resonances are created by the inductance of the slot and the capacitance.
between patches. If the slots are close to the center of the patch, currents on the patch will directly flow through the via and the resonant modes will not be excited. Figure 6 shows the dispersion diagram of the EBG cells with slots. The bandgaps of the design with slots and that of reference design shown in Figure 1 with geometrical parameters (a) are alike, but multiple modes excited between 2 and 4 GHz shift the propagation mode from 5.9 to 7.2 GHz as Figure 7. As the propagation mode at 7.2 GHz still limits the stopband bandwidth, the structure comprises EBG cells with slots and the design of discontinuity of impedance between plane pairs is proposed in Section III.

### III. Suppression of Noise Propagation by the Proposed EBG Structure

The proposed structure is composed of slots on patches with thickened dielectric design for broadband suppression of noise propagation. The thickened dielectric is intended for the enhancement of noise isolation at low frequencies and the slots are responsible for the suppression of high frequency noise by excited
multiple modes. Figure 8 shows the proposed structure can effectively suppress the cavity noise under $-35$ dB over the frequency range from 0.56 to 20 GHz. An experiment board shown in Figure 9 is designed for the validation of the proposed structure. $5 \times 5$ EBG cells are placed on a four-layer PCB with $6 \text{ cm} \times 6 \text{ cm}$ substrate. The cavity noise is injected and picked by two compression-mount SMA connectors, respectively. Figure 10 shows excellent correlation result between simulation and measurement. The measurement result proves the cavity noise can be suppressed under $-35$ dB from 0.56 GHz to the highest measurement frequency, 20 GHz.

**IV. CONCLUSION**

The cavity noise propagating between two planes can be picked by layer transition vias passing through the cavity. In this paper, an EBG structure based on interleaved and double stacked patches is proposed to suppress the cavity noise. The increased inductance of the proposed EBG cell shifts the resonant frequency to lower frequency and the multiple modes excited by additional slots shift propagation mode to higher frequency. The stopband bandwidth is then extended for the broadband suppression of the cavity noise. The simulation and measurement results indicate that the cavity noise is suppressed under $-35$ dB from 0.56 GHz to the highest measurement frequency, 20 GHz.

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**REFERENCES**


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