

## Polarization Insensitive Compact Chipless RFID Tag

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**Abstract** — This research article proposes a highly dense, inexpensive, flexible and compact 29 x 29 mm<sup>2</sup> chipless radio frequency identification (RFID) tag. The tag has a 38-bit data capacity, which indicates that it has the ability to label 2<sup>38</sup> number of different objects. The proposed RFID tag has a bar-shape slot/resonator based structure, which is energized by dual-polarized electromagnetic (EM) waves. Thus, portraying polarization insensitive nature of the tag. The radar cross-section (RCS) response of the proposed tag design is analyzed using different substrates, i.e., Rogers RT/duroid<sup>®</sup>/5880, Taconic (TLX-0), and Kapton<sup>®</sup>HN (DuPont<sup>™</sup>). A comparative analysis is done, which reveal the changes observed in the RCS curve, as a result of using different substrates and radiators. Moreover, the effect on the RCS response of the tag is also examined, by bending the tag at different bent radii. The compactness and flexible nature of the tag makes it the best choice for Internet of things (IoT) based smart monitoring applications.

**Index Terms** — Chipless tag, Radar Cross-Section (RCS), Radio Frequency Identification (RFID).

### I. INTRODUCTION

The continuous evolution of Internet of things (IoT) is the outcome of its tremendous scope in the present technological era. The fundamental concept of IoT is to create a web of interconnected smart objects [1]. Thus, IoT guarantees to provide every object with a communication capability and radio frequency identification (RFID) is the key enabler that gives a taste of reality to this aspect [2]. Hence, with the ever growing communication technology, IoT has been the center of attention for researchers to provide real-time solutions for the welfare of humanity [3].

RFID is one of the leading facilitator of IoT, which has spell-bound the global market due to its affordability, increased efficiency, compact size, and numerous applications in logistic [4], supply chain management,

vehicle identification [5], baggage tracking on airports, etc. Furthermore, in recent years in order to meet the growing IoT trends, sustained advancement is observed from chip-based to chipless RFID technology. Chip-based tags are not much preferred because of the embedded silicon IC, which ultimately leads to higher price tags that are not suitable for a number of low-cost applications [6]. The identification cost should not be greater than the worth of object/item to be tagged.

Chipless tags do not have an integrated chip (IC) and therefore can be printed easily on to the goods. Moreover, this has also reduced their cost as compared to the chip-based counterparts. Consequently, the research efforts are more concentrated towards the development of low-cost, passive chipless RFID tags.

Various researches have revealed different passive chipless tags having compact and polarization insensitive geometries. In [7], a cross loop resonator based tag with a polarization independent nature is presented. It has the ability to store 20-bit data within a footprint of 4 x 4 cm<sup>2</sup>. Then in [8], a chipless tag; consisting of multiple circular ring patch resonators, insensitive to polarization, is disclosed. The tag is designed in a compact dimension of 3 x 3 cm<sup>2</sup> with a coding capacity of upto 19 data bits.

This research work proposes a novel, compact, polarization insensitive, passive chipless RFID tag, comprising of a bar-shape slot/resonator based structure. The proposed tag is capable of yielding 38-bit high data capacity within a compact size of 29 x 29 mm<sup>2</sup>. The tag is analyzed for various substrates along with different materials as radiators. Initially, the tag is designed using Rogers RT/duroid<sup>®</sup>/5880 substrate with copper metal as radiator in a frequency range of 4.6–14.3 GHz. Another tag is inspected using Taconic (TLX-0) along with copper radiator, and its response is analyzed for a frequency range of 4.62–14.4 GHz. Furthermore, to achieve flexibility within a reasonable budget, the tag is also examined using Kapton<sup>®</sup>HN substrate along with aluminum and silver nano-ink (Cabot ink CCI-300) as a radiator. The

RF range for Kapton<sup>®</sup>HN along with aluminum radiator is 5.4–17.97 GHz and 5.3–17.95 GHz for silver nano-ink.

## II. WORKING MECHANISM

The passive chipless RFID tag/transponders do not need a battery to power them up [9]. They communicate using the principle of backscattering [10]. The proposed RFID tag has a dual-polarized slot/resonator based structure which means that the tag consists of horizontal and vertical slots. Consequently, the reader circuitry comprises of horizontally and vertically polarized transmitting and receiving antennas. The horizontally polarized transmitting antenna powers up the horizontal slots and the vertically polarized transmitting antenna excites the vertical slots of the RFID tag. Ultimately, the horizontal and vertical receiver antennas, present on the reader side, gather the modulated backscattered signals from the slots arranged in two different orientations. Besides, the dual-polarized structure of the tag depicts its polarization insensitive nature. Owing to the fact, and unlike [11], the length of the horizontal slots is kept different from the length of vertical slots in the proposed tag structure, consequently they resonate at different frequencies which prevents mutual interference between the slots. Thus, the proposed tag always yields 38-bits, irrespective of the angle, to which it may be rotated. Ultimately, it can be used as 38-bit polarization insensitive tag. Moreover, the sharpness and the clarity of the resonant dips is not effected by rotating the tag. The RCS response of a single slot/resonator of the tag i.e.,  $S_{27}$  and  $S_{28}$  on H and V-probe, at different angles of rotation is shown in Fig. 1.

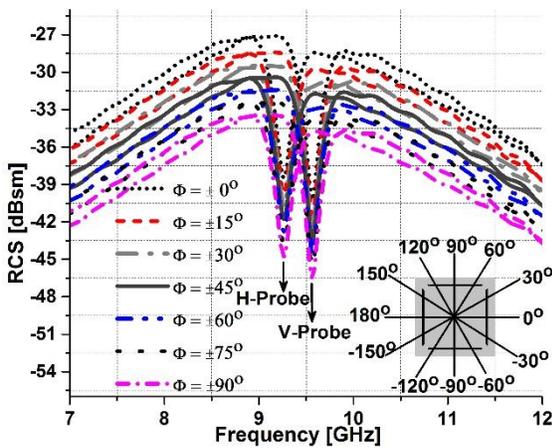


Fig. 1. Polarization insensitivity depicted by the tag.

The E-field circular wave equation is stated as:

$$\mathbf{E}(x, y, z, t) = E_o \exp[j(\omega t - kz)]\hat{\mathbf{x}} + E_o \exp\left[j\left(\omega t - kz + \frac{\pi}{2}\right)\right]\hat{\mathbf{y}}, \quad (1)$$

where  $\mathbf{E}$  represents the electric field,  $\omega$  shows the angular frequency,  $(x, y, z)$  represents the position vector

and  $k$  constitutes the wave number.

## III. PROPOSED CHIPLESS TAG DESIGN

Researchers have explored different aspects of the chipless technology. Efforts have been made to cater high data capacity requirement. The novelty of the proposed tag lies in its flexible nature, along with the ability to yield 38-data bits in a compact size while remaining insensitive to polarization. The tag is designed using CST STUDIO SUITE<sup>®</sup>. Furthermore, the flexible Kapton<sup>®</sup>HN substrate based tag is printed with the help of DMP2800 inkjet printer which consists of a single refillable piezo-based print cartridge having 16 nozzles in a single row. The nozzles have a spacing of 254  $\mu\text{m}$  and can eject drops within a range of 1pl to 10pl subjected to the type of cartridge used. The proposed tag is printed using 10pl cartridge (DM-11610) filled with silver nano-ink, i.e., (Cabot CCI-300). Moreover, the sintering process is carried out at 150  $^{\circ}\text{C}$  for 2 hours. The printing accuracy is determined by observing the tag under ULTRA-55 Field Emission Scanning Electron Microscope from Carl Zeiss NTS. The 29 x 29  $\text{mm}^2$  chipless tag is capable of encoding 38-bit data which indicates that it can tag  $2^{38}$  number of items/objects. In this research article, the tag design consists of horizontal and vertical bar-shaped slot/resonators of varying lengths, which are arranged in a square shape fashion. The resonating structures are meant for storing the encrypted information. When the EM waves strike these resonators then, each slot, i.e., (gap between the metallic parts), corresponds to one dip, which ultimately represents one bit or a logic state '1'. Subsequently, a logic state '0' is used to represent a slot that has been shorted. The proposed tag design is shown in Fig. 2.

The tag contains thirty-eight slot resonators, out of which nineteen are horizontally polarized, and nineteen are vertically polarized. The slots are represented as  $S1$ - $S38$ . The horizontal slot resonators are odd numbered and are labeled as  $S1$ ,  $S3$  onwards. Horizontally polarized plane wave energizes them. Whereas, the vertical slot/resonators are even numbered and are labeled as  $S2$ ,  $S4$  onwards. They are excited by the vertically polarized plane waves. The metallic parts of the tag are referred as  $M1$ - $M38$  each having width of 0.2 mm. The slot  $S1$  and  $S2$  have 8.3 mm and 8.2 mm width, respectively. Besides, all the slots from  $S5$ - $S38$  have a constant width of 0.3 mm, whereas, slot  $S3$  and  $S4$  are optimized to 0.5 mm width. Since, keeping their width equal to 0.3 mm, affects their corresponding resonant dips, and makes them hardly detectable on RCS curve; due to mutual interference. Thus, the widths of all the slots are optimized in such a way, so that sharp and clear resonant dips are achieved on the RCS plot. Furthermore, the tag is kept at a far-field distance from the reader to measure its radar cross section (RCS) response. The far-field distance of the tag can be calculated from (2):







The experimental setup used for testing of the tag consists of a Vector Network Analyzer (VNA) R&S®ZVL13 and two horn antennas, one is transmitter and the other is receiver. Transmitter antenna directs interrogator signal on the transponder, set at a far-field distance. The receiver antenna then collects the backscattered signal encoded with unique identification code using VNA.

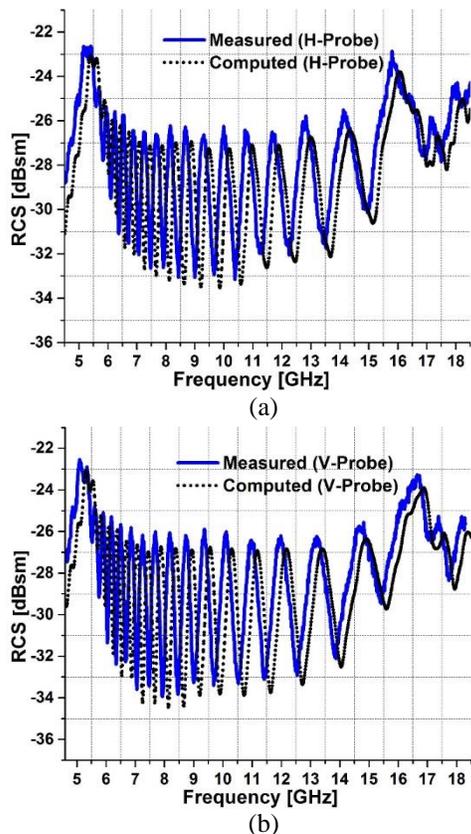


Fig. 10. (a) Measured RCS response (H-Probe), and (b) measured RCS response (V-Probe).

Table 2 shows a comparative study of the previous research works and the proposed tag design.

Table 2: Comparison with previous research work

Parameters	I-Slot [11]	Circular [14]	Square [15]	Proposed Research
Size [cm <sup>2</sup> ]	7.29	2.36	9	8.41
Tans. bits	32	9	5	38
Bit density [Bits/cm <sup>2</sup> ]	4.38	3.80	0.55	4.51
Flexibility	x	x	x	✓

From Table 2, it is evident that the proposed tag has the capability to encode 38-bit data in a miniaturized footprint of 8.41 cm<sup>2</sup>. Thus, yielding a high bit density comparatively. Moreover, the flexible characteristic of

the tag allows it to be deployed on irregular surface.

## VII. CONCLUSION

A compact 29 x 29 mm<sup>2</sup> chipless RFID tag, having a high data capacity of 38-bits is revealed in this research work. The proposed tag structure yield very stable resonant dips, when analyzed for multiple substrates. The tag analyzed using Kapton®HN substrate is found to be of utmost significance; because of its flexible nature, easy printability and stability toward thermal changes. The tag analyzed using Rogers RT/duroid®/5880 substrate exhibits electrical stability towards high humidity levels. Consequently, providing an optimal solution for moisture rich atmospheres. Thus, the proposed tag can be used in different environmental conditions; becoming the ideal choice for IoT based industrial applications.

## ACKNOWLEDGEMENTS

Vinnova (The Swedish Governmental Agency for Innovating Systems) and UET, Taxila, Pakistan, financially assisted this work.

## REFERENCES

- [1] M. Bolic, M. Rostamian and P. M. Djuric, "Proximity detection with RFID: A step towards the internet of things," *IEEE Pervasive Comput.*, vol. 14, pp. 70-76, 2015.
- [2] A. Habib, M. A. Azam, Y. Amin, and H. Tenhunen, "Chipless slot resonators for IoT system identification," *IEEE Int. Conf. on Electro Inf. Technol. (EIT)*, Grand Forks, ND, pp. 0341-0344, 2016.
- [3] J. Mccann and K. K. Leung, "A survey on the ietf protocol suite for the internet of things: Standards, challenges, and opportunities," *IEEE Wireless Commun.*, vol. 20, pp. 91-98, 2013.
- [4] C. Yuqiang, G. Jianlan, and H. Xuanzi, "The research of internet of things' supporting technologies which face the logistics industry," *Int. Conf. on Comput. Intell. and Security (CIS)*, Nanning, pp. 659-663, 2010.
- [5] A. Toccafondi, C. D. Giovampaola, P. Braconi, and A. Cucini, "UHF-HF RFID integrated transponder for moving vehicle identification," *ACES Journal*, vol. 25, pp. 543-551, June 2010.
- [6] Y. Feng, L. Xie, Q. Chen, and L. R. Zheng, "Low-cost printed chipless RFID humidity sensor tag for intelligent packaging," *IEEE Sensors J.*, vol. 15, pp. 3201-3208, 2015.
- [7] V. R. Sajitha, C. M. Nijas, T. K. Roshana, K. Vasudevan, and P. Mohanan, "Compact cross loop resonator based chipless RFID tag with polarization insensitivity," *Microw. Opt. Technol. Lett.*, vol. 58, pp. 944-947, 2016.
- [8] A. Vena, E. Perret, and S. Tedjini, "High-capacity chipless RFID tag insensitive to the polarization," in *IEEE Trans. Antennas Propag.*, vol. 60, pp.

- 4509-4515, 2012.
- [9] A. Vena, E. Perret, and S. Tedjini, "Toward a reliable chipless RFID humidity sensor tag based on silicon nanowires," *IEEE Trans. Microw. Theory Techn.*, vol. 64, pp. 2977-2985, 2016.
- [10] D. Betancourt, K. Haase, A. Hübler, and F. Ellinger, "Bending and folding effect study of flexible fully printed and late-stage codified octagonal chipless RFID tags," *IEEE Trans. Antennas Propag.*, vol. 64, pp. 2815-2823, 2016.
- [11] M. A. Islam and N. Karmakar, "Compact printable chipless RFID tags using polarization diversity," *42nd European Microw. Conf.*, Amsterdam, pp. 912-915, 2012.
- [12] T. Dissanayake and K. P. Esselle, "Prediction of the notch frequency of slot loaded printed UWB antennas," *IEEE Trans. Antennas Propag.*, vol. 55, pp. 3320-3325, 2007.
- [13] L. Xu and K. Huang, "Design of compact trapezoidal bow-tie chipless RFID tag," *Int. J. Antennas Propag.*, vol. 2015, pp. 7, 2015.
- [14] M. Martinez and D. van der Weide, "Compact slot-based chipless RFID tag," *IEEE RFID Technol. Applicat. Conf. (RFID-TA)*, Tampere, pp. 233-236, 2014.
- [15] F. Costa, S. Genovesi, and A. Monorchio, "A chipless RFID based on multiresonant high-impedance surfaces," *IEEE Trans. Microw. Theory Techn.*, vol. 61, pp. 146-153, 2013.



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