Grid-Connected Solar PV Fed BLDC Motor Drive for Water Pumping System

Biranchi Narayan Kar^{1,*}, Paulson Samuel¹, Amit Mallick² and Jatin Kumar Pradhan²

¹Motilal Nehru National Institute of Technology, Allahabad, Prayagraj, Uttar Pradesh, India ²Veer Surendra Sai University of Technology, Burla, Odisha, India E-mail: bnkar@mnnit.ac.in *Corresponding Author

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Abstract

This article proposes a unidirectional power flow of a grid-connected brushless DC motor powered water pumping system fed by a photovoltaic array using a bridgeless power factor corrected (PFC) boost converter. The system consists of a bridgeless PFC converter, a boost converter, and a voltage source inverter to drive a brushless DC motor coupled with a pump. As a backup source, the grid is used. This system allows the water pump to run at maximum capacity regardless of the weather conditions. The grid will provide power if the photovoltaic array is unable to fulfil the required power demand. The unidirectional power flow through a conventional power factor corrected (PFC) boost converter causes conduction loss in the input bridge rectifier, thereby hurting efficiency, power factor, and THD. This paper presents a

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bridgeless PFC boost converter topology to reduce the conduction losses, thereby increasing the efficiency and obtaining a nearly unity power factor as well as lower total harmonic distortion (THD) of input current. The system is simulated using MATLAB /Simulink. The proposed system's real-time validation is realized through the OPAL-RT simulator OP5700. The THD results obtained are well within the specified standard of IEC 61000-3-2 and IEEE 519-1992.

Keywords: Brushless DC motor, solar photovoltaic, unidirectional power flow control, water pump, power quality.

Variables

 V_{pv}, I_{pv}, P_{pv} – Voltage (V), Current (A), Power(P) of PV Array Vmpp, Impp, Pmpp - Voltage (V), Current (A), Power(P) of PV Array at the maximum powerpoint V_{oc} , I_{oc} – Voltage (V), Current (A)at open circuit Vdc – DC-Link voltage (V) P_h – Pump power (kW) P_m – BLDC motor power(kW) η_p – Pump efficiency η_{motor} – Efficiency of BLDC motor $\eta_{converter}$ – Efficiency power converter C - DC bus capacitor (μF) ω – Speed of BLDC motor (rad/sec) *N* – Speed of BLDC motor (rpm) K - Proportionality constant Ic – Capacitor current (A) I_{inv} – Inverter current (A) Fsw – Switching frequency (Hz) K_{pd} – Proportional gain K_{id} – Integral gain M – Sampling instant T_e – Electromagnetic torque (Nm) N_s, N_p – Number of series and parallel connected module of PV array

Abbreviation

PV array – Photovoltaic array MPPT – Maximum power point tracking BLDC motor – Brushless DC motor PFC boost converter – Power factor corrected boost converter THD – Total harmonic distortion

1 Introduction

The rapid exhaustion of fossil fuel resources will result in a global energy crisis shortly [1]. Hence, the consumer is motivated toward renewable energy. Solar PV technology is the best substitute for the conventional sources of various appliances. Water pumping for irrigation in remote areas is hampered by a lack of electricity and high diesel prices. The uniform chopping in the price of PV modules inspires towards utilization of photovoltaic based water pumping systems [2, 3]. DC motor is utilized at the initial stage for the photovoltaic based water pumping system, followed by an induction motor [4]. For improving the efficiency and performance of a water pumping system fed by a solar photovoltaic array, a brushless dc (BLDC) motor is mostly preferable to an induction motor and DC motor owing to its free maintenance, high efficiency, as well as small size.

Based on a PV array, the water pumping system depends only on solar energy. But the main drawback of solar photovoltaic generation is its intermittency, which results in an inconsistent delivery of water with a standalone system. There is a severe interruption in water pumping because of the bad environmental conditions. Hence, the water cannot be delivered at full capacity by the pump. Furthermore, the water-pumping system stops operating at night due to the unavailability of solar energy. For a reliable PV fed water pumping system, these deficiencies need to be conquered. Hence, this system with a battery backup as energy storage is presented [5]. However, the main drawback of the battery is its maintenance cost and reduced service life [6, 7].

Due to the aforementioned drawbacks of the battery, researchers focused on another option. Hence, the water pumping system is fed by a grid. A grid interfaced water pumping system fed by a photovoltaic array is introduced in [8]. The grid is interfaced with the system through a conventional PFC boost converter with a power flow control. The converter is connected to the grid through a bridge rectifier, which causes high conduction losses. This

causes a decrease in power factor and an increase in THD, resulting in a decrease in efficiency.

A grid-connected brushless DC motor-driven system fed by a photovoltaic array using a bridgeless PFC boost converter for the application of water pumping is proposed in this article. The converter reduces THD, improves power factor, and thus improves the system's efficiency. The power quality of the system is well within the specified standards of IEC 61000-3-2 and IEEE 519-1992. The unidirectional flow of power from the utility grid to the motor pump is enabled by a proposed control when solar power is deficient for delivering water with full capacity. Power transformation from the grid to the system is not required when the availability of solar irradiance is full.

In this article, a DC-DC boost converter is utilized to monitor peak power from a photovoltaic array using an incremental conductance technique [9–11]. The motor speed is not regulated by a phase current sensor in this case. The voltage source inverter is switched via an electronic commutation of the motor at its fundamental frequency, resulting in minimal switching losses. The proposed system is designed, simulated, and validated in real-time through the OPAL-RT simulator.

2 Structure of the System

The structure of the system is demonstrated in Figure 1(i). The system consists of a voltage source inverter (VSI), utility grid, bridgeless PFC boost converter, photovoltaic array, boost converter, BLDC motor coupled with a pump. Through the boost converter and VSI, energy is fed from the photovoltaic array to the BLDC motor. The boost converter uses the INC algorithm to perform MPPT on the photovoltaic array, while VSI is utilized for electronic communication with the BLDC motor. The bridgeless PFC boost converter helps in distributing power between the grid and the photovoltaic array as well as upgrading the power quality of the AC mains.

3 Bridgeless PFC Boost Converter

The topology used for unidirectional power flow to drive the BLDC motor is shown in Figure 2. This topology has an important advantage over the PFC boost converter, i.e., the switches used in the topology have minimum



Figure 1 (i) Structure for water pumping system (ii) Unidirectional power flow.

conduction losses. This is because, at any instant of operation, only two devices are on the current path [12–14]. There are two single boost converters used in this topology without any rectifier. It operates in CCM. Controlling the switches S_A and S_B during all half cycles of ac input voltage regulates the current as well as the output voltage.



Figure 2 Bridgeless PFC boost converter topology.

When Switch S_A is in the on state during the positive half cycle, the current flows through S_A and the body diode of S_B . The stored energy from the inductor and capacitor is released through load during this period $(0 < t < dT_S)$

$$v_s = L_1 \frac{di_s}{dt} \tag{1}$$

$$\frac{dv_0}{dt} = -\frac{v_0}{z_L C_0} - \frac{1}{z_L C_0} e_b \tag{2}$$

When the switch S_A is in an off state, the current passes via D1, the capacitor, and the body diode of the switch S_B . (dTs < t < Ts)

$$v_s - v_0 = L_1 \frac{di_s}{dt} \tag{3}$$

$$\frac{dv_0}{dt} = i_s - \frac{v_0}{z_L C_0} + \frac{1}{z_L C_0} e_b \tag{4}$$

During the negative half of the AC input voltage, when Switch S_B is in the on state, the current flows through S_B and the body diode of S_A . The stored energy from the inductor and capacitor is released through load during this period (0 < t < dTs)

$$v_s = L_2 \frac{di_s}{dt} \tag{5}$$

$$\frac{dv_0}{dt} = -\frac{v_0}{z_L C_0} - \frac{1}{z_L C_0} e_b \tag{6}$$

When the switch S_B is in an off state, the current passes via D1, the capacitor, and the body diode of the switch S_A . (dTs < t < Ts)

$$v_s - v_0 = L_2 \frac{di_s}{dt} \tag{7}$$

$$\frac{dv_0}{dt} = i_s - \frac{v_0}{z_L C_0} + \frac{1}{z_L C_0} e_b \tag{8}$$

During the time interval of 0 < t < dTs and dTs < t < Ts, the mathematical model of converter is given in (9) and (10)

$$\underbrace{\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_0}{dt} \end{bmatrix}}_{\dot{x}_1} = \underbrace{\begin{bmatrix} 0 & 0 \\ 0 & -\frac{1}{z_L C_0} \end{bmatrix}}_{A_1} \underbrace{\begin{bmatrix} i_s \\ v_0 \end{bmatrix}}_{x} + \underbrace{\begin{bmatrix} \frac{1}{L} & 0 \\ 0 & \frac{1}{C_0 z_L} \end{bmatrix}}_{B_1} + \underbrace{\begin{bmatrix} v_s \\ e_b \end{bmatrix}}_{v} \quad (9)$$

$$\underbrace{\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_0}{dt} \end{bmatrix}}_{\dot{x}_2} = \underbrace{\begin{bmatrix} 0 & 0 \\ \frac{1}{C_0} & -\frac{1}{z_L C_0} \end{bmatrix}}_{A_2} \underbrace{\begin{bmatrix} i_s \\ v_0 \end{bmatrix}}_{x} + \underbrace{\begin{bmatrix} \frac{1}{L} & 0 \\ 0 & \frac{1}{C_0 z_L} \end{bmatrix}}_{B_2} + \underbrace{\begin{bmatrix} v_s \\ e_b \end{bmatrix}}_{v} \quad (10)$$

Where, $v_s, i_s, T_s, C_0, z_L = L_l + j\omega L_l, L = L_1 = L_2$, and e_b are source voltage, current, switching period, output capacitance, the impedance of the motor, input inductance, and back emf respectively.

For small-signal analysis, the system is analyzed around the operating point $(\bar{X}, \bar{D}, \bar{V}_s, \bar{e}_b)$ with a small perturbation $(\hat{x}, \hat{d}, \hat{v}, \hat{e}_b)$ and is represented as

$$x = \bar{X} + \hat{x}, \ d = \bar{D} + \hat{d}, \ v_s = \bar{V}_s + \hat{v}, \ e_b = \bar{e}_b + \hat{e}_b$$
 (11)

By substituting Equation (11) in Equations (9) and (10), the modified equation

$$\frac{d}{dt}(\bar{X}+\hat{x}) = [A\bar{X}+B\bar{V}+A\hat{x}+B\hat{v}+[(A_1-A_2)\bar{X}+(B_1-B_2)\bar{V}]\hat{d}$$
(12)

Where,
$$A = (A_1\bar{D} + A_2 - A_2\bar{D}), B = (B_1\bar{D} + B_2 - B_2\bar{D}).$$



Figure 3 Block diagram of control loop of bridgeless PFC boost converter.

Since \bar{X} is the operating point, $\frac{d}{dt}\bar{X} = 0$. With these data, by considering only the information of the operating point, the states can be calculated as

$$\bar{X} = -A^{-1}B\bar{V} = -\begin{bmatrix} \frac{\bar{V}_s}{z_L(1-\bar{D})^2} & \frac{\bar{e}_b}{z_L(1-\bar{D})}\\ \frac{\bar{V}_s}{1-\bar{D}} & 0 \end{bmatrix} \bar{V}$$
(13)

From Equations (12) and (13).

The perturbed small-signal model becomes

$$\frac{d}{dt}\hat{x} = A\hat{x} + B\hat{v} + [(A_1 - A_2)\bar{X} + (B_1 - B_2)\bar{V}]\hat{d}$$
(14)

Since \bar{X} and \bar{V} are known, by using Equation (14), the current and voltage loop transfer function shown in Figure 3 can be obtained.

3.1 Current Control Loop

The transfer function is obtained by assuming $\hat{v} = 0$.

$$\frac{\hat{X}(s)}{\hat{d}(s)}\Big|_{\hat{v}=0} = (sI - A)^{-1}B_d$$
(15)

This yields

$$\left. \frac{\hat{i}_s(s)}{\hat{d}(s)} \right|_{\hat{v}=0} = \frac{s^2 L_l C_0 \bar{V}_s D' + s R_l C_0 \bar{V}_s D' + 2 \bar{V}_s D' - e_b D'^2}{s^3 L_l L C_0 D'^2 + s^2 R_l L C_0 D'^2 + s L D'^2 + R_l D'^4}$$
(16)

Where, D' = 1 - D.

3.2 Voltage Control Loop

The transfer function is obtained from (14) by assuming $\hat{d} = 0$

$$\frac{\hat{X}(s)}{\hat{v}(s)}\Big|_{\hat{d}=0} = (sI - A)^{-1}B$$
(17)

This yields

$$\frac{\hat{X}(s)}{\hat{v}(s)} = \frac{1}{s^2 + \frac{s}{z_L C_0} + \frac{D'^2}{L C_0}} \begin{bmatrix} \left(s + \frac{1}{z_L C_0}\right) \frac{1}{L} & \frac{-D'}{L} \frac{1}{C_0 z_L} \\ \frac{D'}{L C_0} & \frac{s}{C_0 z_L} \end{bmatrix}$$
(18)

The transfer function in Equation (18) can be simplified as

$$\hat{X} = \begin{bmatrix} \frac{s^2 L_l C_0 + s R_l + L}{\Delta} & -\frac{D'}{\Delta} \\ \frac{s D' L_l + D' R_l}{\Delta} & \frac{s L}{\Delta} \end{bmatrix} \begin{bmatrix} V_s \\ e_b \end{bmatrix}$$
(19)

Where $\Delta = s^3 L_l L C_0 + s^2 R_l L C_0 + s(L + L_l D'^2) + R_l D'^2$. The Equation (19) can be separated as

$$\hat{i}_s = \frac{s^2 L_l C_0 + s R_l C_0 + L}{\Delta} v_s - \frac{D'}{\Delta} eb$$
⁽²⁰⁾

$$\hat{v}_0 = \frac{sD'L_l + D'R_l}{\Delta}v_s + \frac{sL}{\Delta}eb$$
(21)

By combining (20) and (21), the transfer function becomes

$$\left. \frac{\hat{v}_0}{\hat{i}_s} \right|_{\hat{e}_b = 0} = \frac{sD'L_l + D'R_l}{s^2 L_l C_0 + sR_l C_0 + L}$$
(22)

4 System Design

4.1 Design and Estimation of PV Array

For a motor rated at 1.1 kW, a 1.26 kW photovoltaic array is designed. The higher rating of the photovoltaic array is chosen to compensate for converter losses. A PV module with a V_m and I_m of 15.14 V and 4.3 A was

selected here. The MPP voltage of the PV array is chosen as $V_{mpp} = V_{pv} = 300$ V. Other parameters are estimated as

$$I_{mpp} = i_{pv} = \frac{P_{mpp}}{V_{mpp}} = \frac{1261}{300} = 4.2 \text{ A}$$
 (23)

Where $P_{mpp} = P_{pv}$ is the photovoltaic power at MPP. Number of series-connected modules

$$N_s = \frac{V_{mpp}}{V_m} = \frac{300}{15.14} = 19.44 \approx 20 \tag{24}$$

Number of Parallel connected modules

$$N_p = \frac{4.3}{4.2} = 1.02 \approx 1 \tag{25}$$

4.2 Design of DC-DC Boost Converter

The estimation of the input inductor is part of its design. The duty cycle is evaluated as [15, 16]

$$D = \frac{V_{dc} - V_{pv}}{V_{dc}} = \frac{500 - 300}{500} = 0.4$$
(26)

Here, V_{dc} is the DC link voltage. L_3 is evaluated as

$$L_3 = \frac{DV_{pv}}{f_{sw3} \times \Delta I_L} = \frac{0.4 \times 300}{10000 \times 4.3 \times 0.3} = 6.9 \text{ mH}$$
(27)

Where f_{sw1} is the switching frequency and ΔI_L is the in inductor current ripple.

4.3 Design of Bridgeless PFC Boost Converter

Considering CCM operation, the value of inductor L, for bridgeless PFC power boost converter, is calculated as [17]

$$L = L_1 = L_2 = \frac{V_{dc}}{4f_{sw}\Delta I_{L\,\text{max}}} = \frac{500}{4 \times 10000 \times (5.7 \times 0.2)} = 10.9 \text{ mH}$$
(28)

Where f_{sw} is the switching frequency and ΔI_{Lmax} is the maximum allowed current ripple through the inductor.

4.4 Design of DC-link Capacitor

Its value was evaluated as [18]

$$C = \frac{I_{dc}}{2w_L \Delta V_{dc}} = \frac{2.6}{2 \times (2\pi \times 50) \times 500 \times 0.05} = 167 \,\mu\text{F}$$
(29)

Where I_{dc} is a DC bus average current, ω_L is the line frequency rad/sec. ΔV_{dc} is DC bus voltage ripple.

4.5 Design of Water Pump

The selection and design of the water pump based on its torque-speed characteristic as [19]

$$K_p = \frac{T_L}{\omega^2} = \frac{3}{(2 \times \pi \times 3000/60)^2} = 3.04 \times 10^{-5}$$
(30)

 K_p , T_L and ω are pump constant, load torque in N-m and speed in rad/se.

4.6 Power Stage Design

4.6.1 Inductor loss

The inductor RMS current and the corresponding copper loss are [20]:

$$I_{L.rms} = \frac{P_0}{V_{ac.\,\rm min}} = \frac{1100}{90} = 12.2A\tag{31}$$

$$P_{L.cond} = I_{L.rms^2} \cdot R_{ac} = (12.2)^2 \cdot 0.07 = 10.4W$$
(32)

4.6.2 MOSFET loss

$$I_{s.rms} = \frac{P_0}{V_{ac.\min}} \sqrt{1 - \frac{8.\sqrt{2}.V_{ac.\min}}{3.\pi.V_0}}$$
$$= \frac{1100}{90} \sqrt{1 - \frac{8.\sqrt{2}.90}{3.\pi.500}} = 9.5A$$
(33)

$$P_{s.cond} = I_{s.rms^2} \cdot R_{on(100^{\circ}C)} = (10.9)^2 \cdot (0.045 \cdot 1.8) = 10W$$
(34)

(Assuming $R_{on(100^{\circ}C)} = 1.8 \cdot R_{on(25^{\circ}C)}$)

The average input current is given as:

$$I_{L.avg} = \frac{P_0}{V_{ac.\,\min}} \frac{2 \cdot \sqrt{2}}{\pi} = 10.9A \tag{35}$$

$$P_{s.on} = 0.5 \cdot I_{L.avg} \cdot V_0 \cdot t_{on} \cdot f$$

= 0.5 \cdot 10.9 \cdot 500 \cdot 24.2 \cdot 10^{-9} \cdot 10000 = 0.6W (36)

$$P_{s.off} = 0.5 \cdot I_{L.avg} \cdot V_0 \cdot t_{off} \cdot f$$

= 0.5 \cdot 10.9 \cdot 500 \cdot 54 \cdot 10^{-9} \cdot 10000 = 1.5W (37)

$$P_{s.total} = P_{s.cond} + P_{s.on} + P_{s.off} = 10 + 0.6 + 1.5 = 12.1W$$
(38)

4.6.3 Diode loss

The diode conduction loss can be obtained as

$$I_{D.avg} = \frac{P_0}{V_0} = \frac{1100}{500} = 2.2A \tag{39}$$

$$P_{D.cond} = I_{D \cdot avg} \cdot V_{f \ Diode} = 2.2 \cdot 1.5 = 3.3W \tag{40}$$

The Switching loss is

$$P_{S.Swit} = 0.5 \cdot V_0 \cdot Q_c \cdot f = 0.5 \cdot 500 \cdot 24 \cdot 10^{-9} \cdot 10000 = 0.06W$$
(41)

$$P_{D.total} = P_{D.cond} + P_{D.swit} = 3.3 + 0.06 = 3.36W$$
(42)

5 Control Schematic

5.1 Electronic Communication of BLDC Motor

Voltage source inverter is used to drive the motor, which is operated by an electronic commutation system [21]. Electronic commutation uses a decoder logic circuit to commutate the currents flowing through its windings in a predetermined sequence [22]. The input currents are symmetrically placed at the centre of the back emf for 120° . Three hall signals at an interval of 60° are generated by three hall sensors in consonance with the rotor position [23, 24]. Using a decoder circuit, these three signals are then converted into six switching pulses for a voltage source inverter at the fundamental frequency.

5.2 Speed Control of Motor-Pump

Since grid power support is feasible, the common bus voltage is set to its rated value for supplying the full volume of water. This allows the brushless DC motor to operate at its rated speed regardless of the environmental conditions. The phase current sensor is removed from the motor drive in this case. The bus voltage is not sustained at its rated value when utility grid power is unavailable. As a result, the speed is controlled by an inconsistent bus voltage that varies depending on the amount of solar irradiation available [25].

5.3 Unidirectional Power Flow Control

The power flow from the grid to the DC bus is regulated by the proposed control instantaneously. The control system is demonstrated in Figure 1(ii). The power flow control system has a two cascade control architecture consisting of a PI controller. The real DC bus voltage V_{dc} is compared with the reference value, and the difference is delivered to the PI controller for voltage regulation. As a result, regardless of operating conditions, the bus voltage is maintained. The controller output is multiplied with sin ω t to generate a reference current iref. The real sensed source current 'is' is compared to the reference current, and the difference is delivered to a PI controller [26]. The duty cycle *d* is fed to the PWM generator, which generates pulses for the converter.

6 Simulation Results and Discussion

The proposed system is demonstrated in different states of operation using the MATLAB/Simulink platform, and comparative evolution of power quality using the different converters is presented.

6.1 Starting and Steady Performance

6.1.1 Motor-pump fed by only PV array

The various photovoltaic array, as well as motor parameter responses, can be seen in Figure 4. The PV array produces 1.26 kWp at MPP with a 1000 w/m² irradiance, as shown in Figure 4(i). Hence, the motor operates at the speed and torque of its rated value, i.e. 3000 rpm, 3.5 N-m, which results in the delivery of full capacity of water. The motor parameters, i.e. bus voltage, V_{dc} , speed, N, torque, T, back emf, *ea*, winding current, i_{sa} , shown in Figure 4(i). There is no need for grid power since the photovoltaic array provides enough power for the motor pump.





Figure 4 Steady-state performance of the system (i) Photovoltaic responses (ii) BLDC motor responses.

6.1.2 Motor-pump fed by the only grid

The BLDC motor-pump extracts full power from the utility grid when there is a need for water pumping at night. A sinusoidal source current is extracted from the utility grid and modulates the bus voltage of 500 V represented in Figure 5(i). The motor achieved the speed and torque of rated the value shown in Figure 5(ii).



(ii)

Figure 5 Steady-state performance of the system (i) Grid responses (ii) BLDC motor responses.

6.2 Dynamic Performance

A dynamic condition is described as a rapid change in solar irradiance caused by a change in environmental conditions. The proposed system is demonstrated by considering that a 1000 W/m² of irradiance is available up to 0.2 sec as demonstrated in Figure 6(i). Hence, there is no requirement for utility grid support and the grid current is not drawn as presented in Figure 6(ii). Since solar insolation drops from an irradiance of 1000 w/m² to half value i.e.500 w/m² in 0.2 seconds, the photovoltaic power needed to run the motor pump at full capacity is presumed to be insufficient. The utility grid now supplies power. As a result, the motor receives power from both



Figure 6 Continued



Figure 6 Dynamic state performance of the system (i) Photovoltaic responses (ii) Grid responses (iii) BLDC motor response.

the grid and the photovoltaic array. After 0.2 seconds, a source current is drawn from the grid, maintaining the bus voltage of 500 V, as demonstrated in Figure 6(ii). Hence, the BLDC motor pump runs at speed of 3000 rpm, and the delivery of water is not disrupted, as can be seen in Figure 6(iii).

6.3 Power Quality Aspect Comparative Study

The grid power quality and the efficiency of the system using a converter topology i.e., bridgeless PFC boost converter is compared with a system using a conventional converter. In the Table 1 the comparison is presented. It is found that the proposed system reduced THD from 4.75% to 3.34%, improved the power factor to 0.99 and consequently improve the efficiency to 99.8% than the conventional PFC boost converter. The source current, its harmonic spectrum, and THD of both bridgeless and conventional boost converters are demonstrated in Figure 7.



Figure 7 Source current and THD of (i) Conventional PFC boost converter (ii) Bridgeless PFC boost converter.

6.4 Robustness Plot

Figure 8 show the bode plot of the inner current control loop and outer voltage control loop, respectively.

The bode plot of the voltage control loop has a phase margin of 87.1 deg indicating the system is robust enough to mitigate the unmodelled dynamics and uncertainties. Similarly, the bode plot of the current control loop ensures that the system is robust and stable, with a cross over frequency of 7.93 kHz and a phase margin of 90.2 degrees.

7 Real-Time Implementation with OPAL-RT

The OPAL-RT (OP-5700) HIL simulator has been used to validate the realtime performance of the proposed work. The simulator is a state of the art real-time simulator with inbuilt support for simulink model. RT-Lab software which is bundled with the simulator has been used to implement the simulink

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Figure 8 Bode plot of (i) outer voltage loop (ii) inner current loop.



Figure 9 OPAL-RT simulator OP5700 interfaced with console PC.

model in the simulator. The simulator uses a Xilinx made Virtex 7 FPGA board for real-time simulation. The simulator has an Intel Xeon E5 processor with 16 cores (each core having a speed of 3.2 GHz) and 32 GB DRAM. The real-time simulator has analog I/O channel which can generate voltage in the range of +/- 16 V. To observe the output voltage in the DSO a standard DB37 breakout board has been used, which can be connected to the I/O channels of the simulator.

7.1 Starting and Steady-state Performance

7.1.1 Motor-pump fed by only PV array

The photovoltaic array provides the power needed for the water pump to operate under full irradiance conditions as presented in Figure 10(i). A PV



Figure 10 Real-time responses with OPAL-RT (i) Photovoltaic parameters (ii) BLDC motor parameters.

power of 1.26 W is available under full irradiance conditions. As a result, the motor pump is set to run at 3000 rpm with a torque of 3.5 Nm. The motor parameters i. bus voltage, V_{dc} , speed, N, torque, T, back emf, ea, winding current, i_{sa} as presented in Figure 10(ii).

7.1.2 Motor-pump fed by only grid

During night time when there is a requirement for water pumping, grid supplies full power to the motor-pump as presented in Figure 11(i). Grid supply a sinusoidal current of 6.8 A, when controlling the bus voltage of 500 V, as conferred in Figure 11(i). The motor pump attains a speed and torque of rated value, i.e. 3000 rpm and 3.5 Nm, as depicted in Figure 11(ii).

7.2 Dynamic State Performance

The dynamic conditions are realized by variations in weather conditions. Depending on the climate conditions the grid supplies power to the motor pump. As depicted in Figure 12(i) the solar irradiance suddenly drop from



 $\label{eq:Figure 11} Figure \, 11 \quad \mbox{Real-time responses with OPAL-RT (i) Grid parameters (ii) BLDC motor parameter.}$



Figure 12 Continued



(iii)

Figure 12 Real-time responses with OPAL-RT (i) Photovoltaic parameters (ii) Grid parameters (iii) BLDC motor parameters.

1000 W/m² to its half value i.e. 500 W/m² at 0.2 sec. Before 0.2 seconds, the solar photovoltaic array supplies enough power to the pump to operate at full extent. However, because of decreases in solar insolation to 500 W/m², the unidirectional power control enables the grid to supply power as conferred in Figure 12(ii). Hence the motor pump runs at its rated value to supply the full volume of water consistently shown in Figure 12(iii).

8 Conclusion

An improvement to the power quality of the grid by using a bridgeless PFC boost converter for the proposed system is tested using MATLAB/ Simulink. An OPAL-RT simulator, OP5700, is used to validate the system in real-time. The utility grid has been supported as a power backup to the motor pump. The bridgeless PFC boost converter with power flow control shares the required power depending on the environmental conditions for pumping the water uninterruptedly at the full capacity of the motor pump. Through simulation studies, the performance of the proposed bridgeless PFC boost converter topology is compared with that of the PFC boost converter topology. The results show that a PF of 0.9, a THD of nearly 3.34% and an efficiency

of 99% can be achieved by using a bridgeless PFC boost converter. It is found that this converter has provided much better performance in terms of power factor, third harmonic distortion and efficiency as compared to the PFC boost converter topology. The power quality of the system is well within the specified standard of IEC 61000-3-2 and IEEE 519-1992 under all working conditions.

Appendix

1. Parameters of the solar PV module:

Table 2	
Open circuit voltage, Voc	19.8 V
Short circuit current, I_{sc}	4.8 A
MPP voltage, V_m	15.14 V
MPP current, I_m	4.3 A

2. Parameters of the motor:

Table 3	
Power, P_m	1.1 kW
Speed, N	3000 rpm
Torque, T	3.5 Nm
Pole	4
Stator resistance, Rs	$2.87~\Omega$
Stator inductance, Ls	8.5 mH

3. Parameters of the Grid:

Table 4	ļ
Voltage	230 V
Frequency	50 Hz
Phase	Single

4. Rating and specifications of switches

MOSFET	IRF830,500V,4.5 A
Diode	MUR1560,400-600V,15A

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Biographies



Biranchi Narayan Kar received his BE in Electrical Engineering from Seemanta Engineering College, Jharpokharia, Odisha, India in 2005 and an M.Tech in Power Electronics and Drives from National Institute of Technology Rourkela, Odisha, India in 2011. He is currently working toward his PhD in the Electrical Engineering at Motilal Nehru National Institute of Technology, Allahabad, Pryagraj, Uttar Pradesh, India. His research interests include power electronics Drives and Renewable Energy.



Paulson Samuel received the Bachelor of Engineering (Electrical) degree from the G. S. Institute of Technology and Science, Indore, India, in 1984,

and the Master of Engineering in Computer Science and Engineering degree from Motilal Nehru National Institute of Technology, Allahabad, India, in 1998. He completed his PhD in electrical engineering at Motilal Nehru National Institute of Technology, Allahabad, India, in 2013. From 1990 onward, he has been a faculty member in the Department of Electrical Engineering, Motilal Nehru National Institute of Technology, where he is presently a Professor. From 1984 to 1990, he was an Engineer at the National Thermal Power Corporation, New Delhi, India. His research interests include power quality, distributed generation, automation, control of power converters, and multilevel inverters.



Amit Mallick received his B.Tech in Electrical Engineering from NM Institute of Engineering and Technology, Bhubaneswar Odisha, India in 2013 and his MTech in Power System from VSSUT, Burla, Odisha, India in 2011. He is currently working as Assistant Professor in the Electrical Engineering Dept at VSSUT, Burla, Odisha. His research interests include Power electronics, Power systems and Microgrid operation and protection.



Jatin Kumar Pradhan received his BE in Electrical Engineering VSSUT, Burla, Odisha in 2008, MTech in Power Electronics and Drives from National

Institute of Technology Rourkela, Odisha, India in 2011 and PhD from IIT Bhubaneswar, Odisha, India in 2019. He is currently working as Assistant Professor in the Electrical Engineering Dept at VSSUT, Burla, Odisha, India. His research interests include Control of MIMO System, Robust Control, Control System Applications.